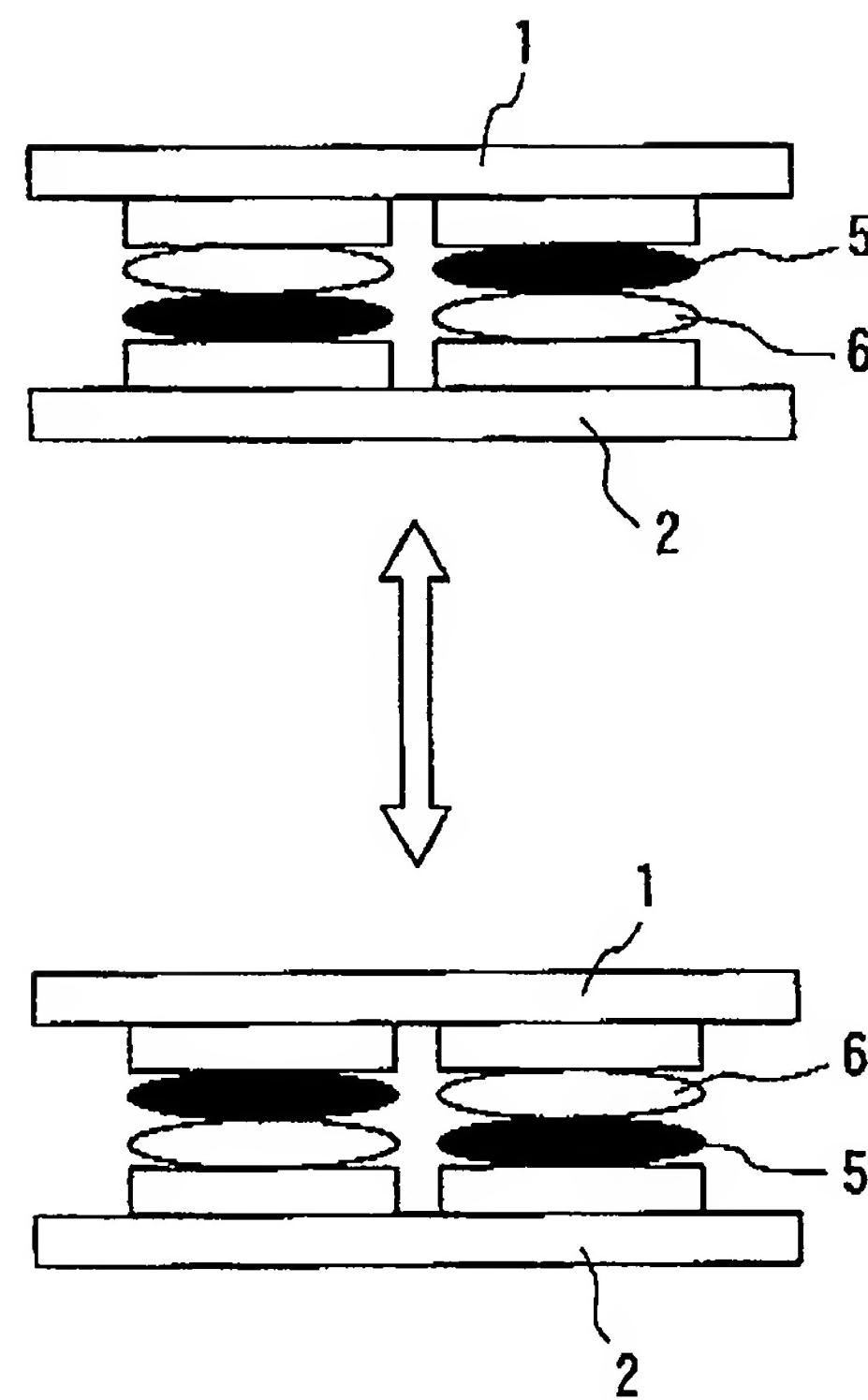


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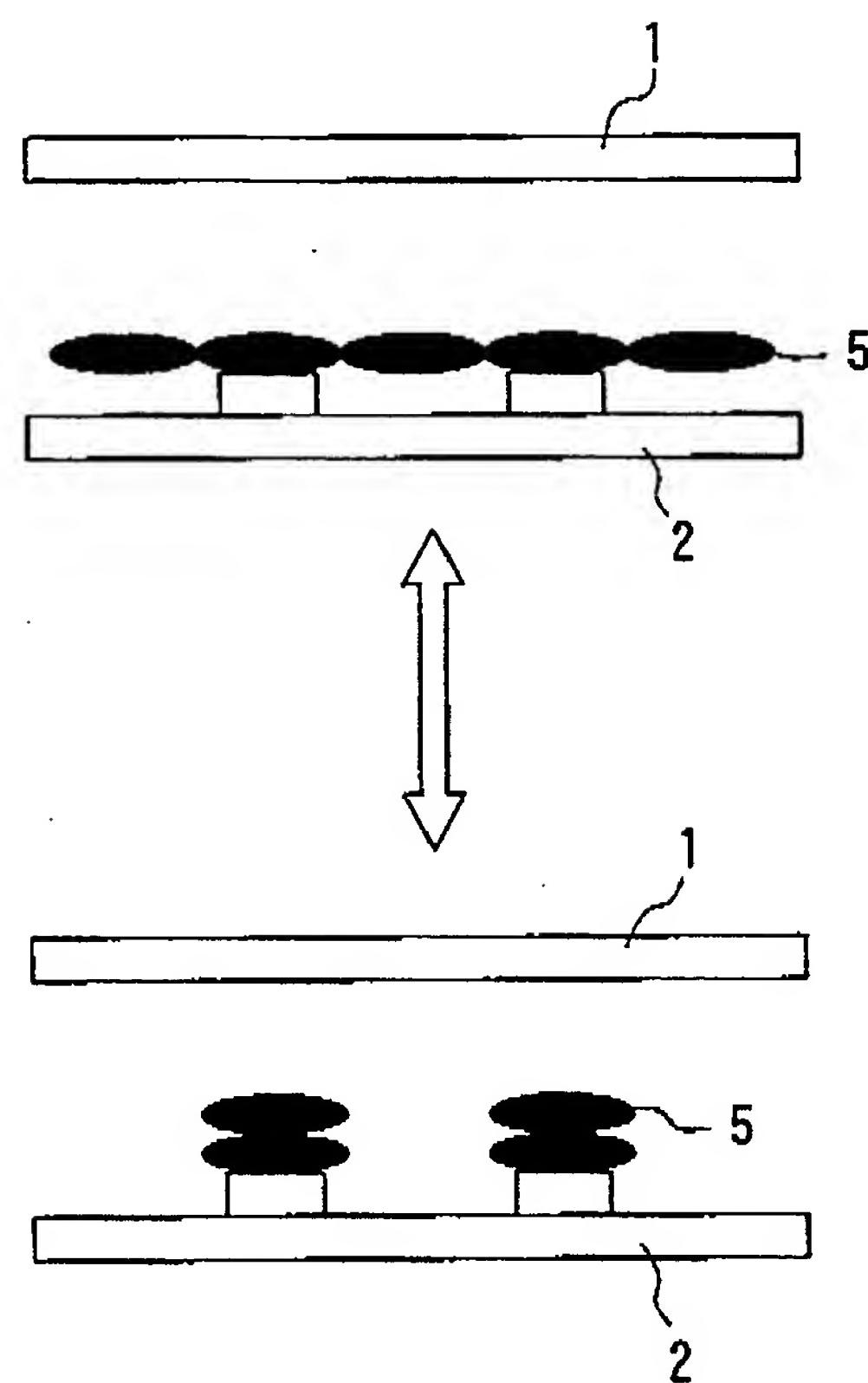
FIG. 1



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FIG. 2

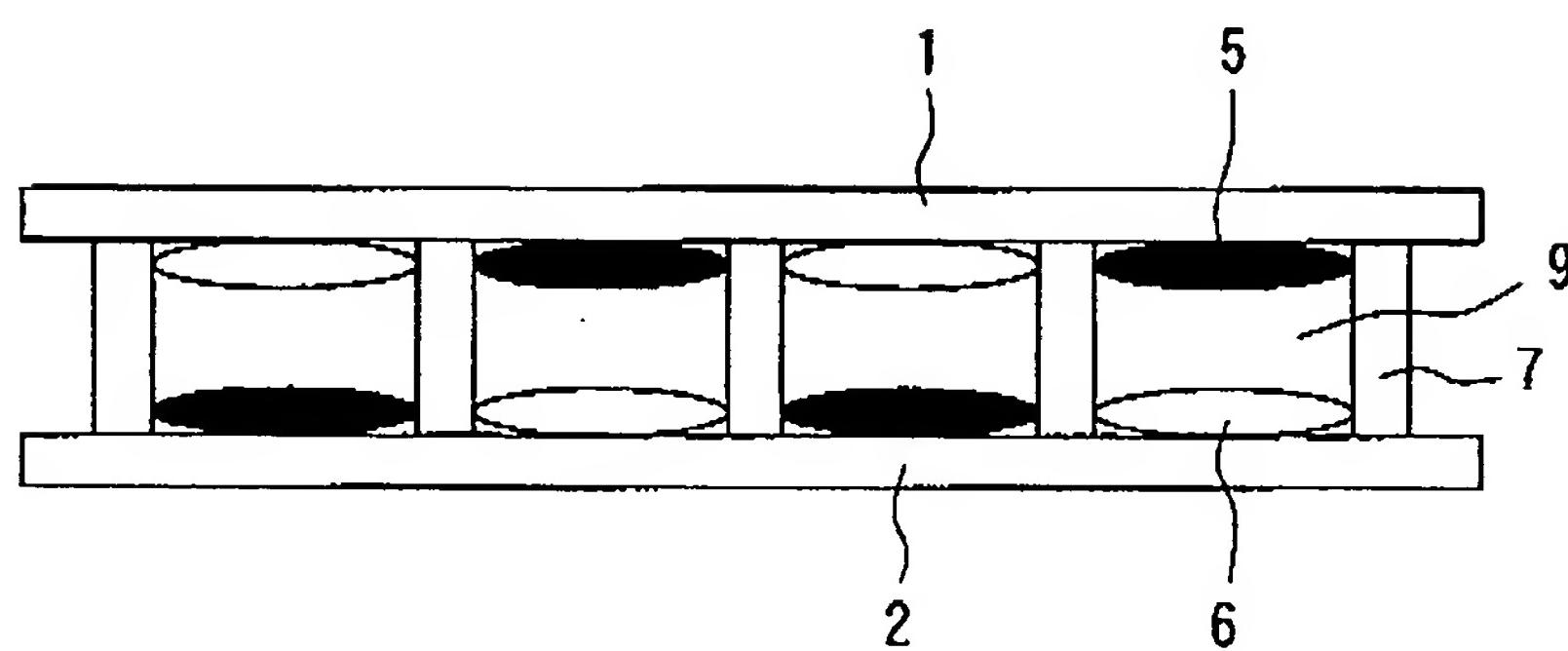


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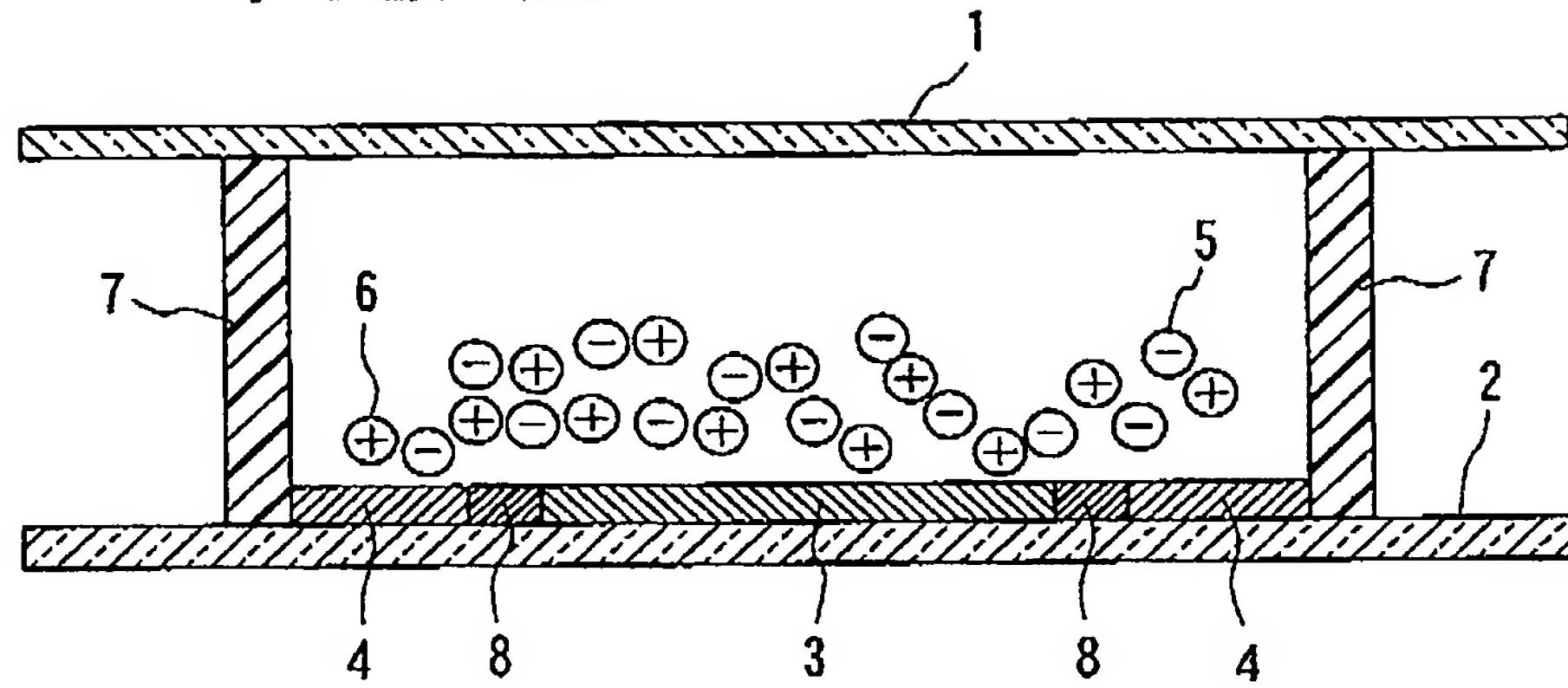
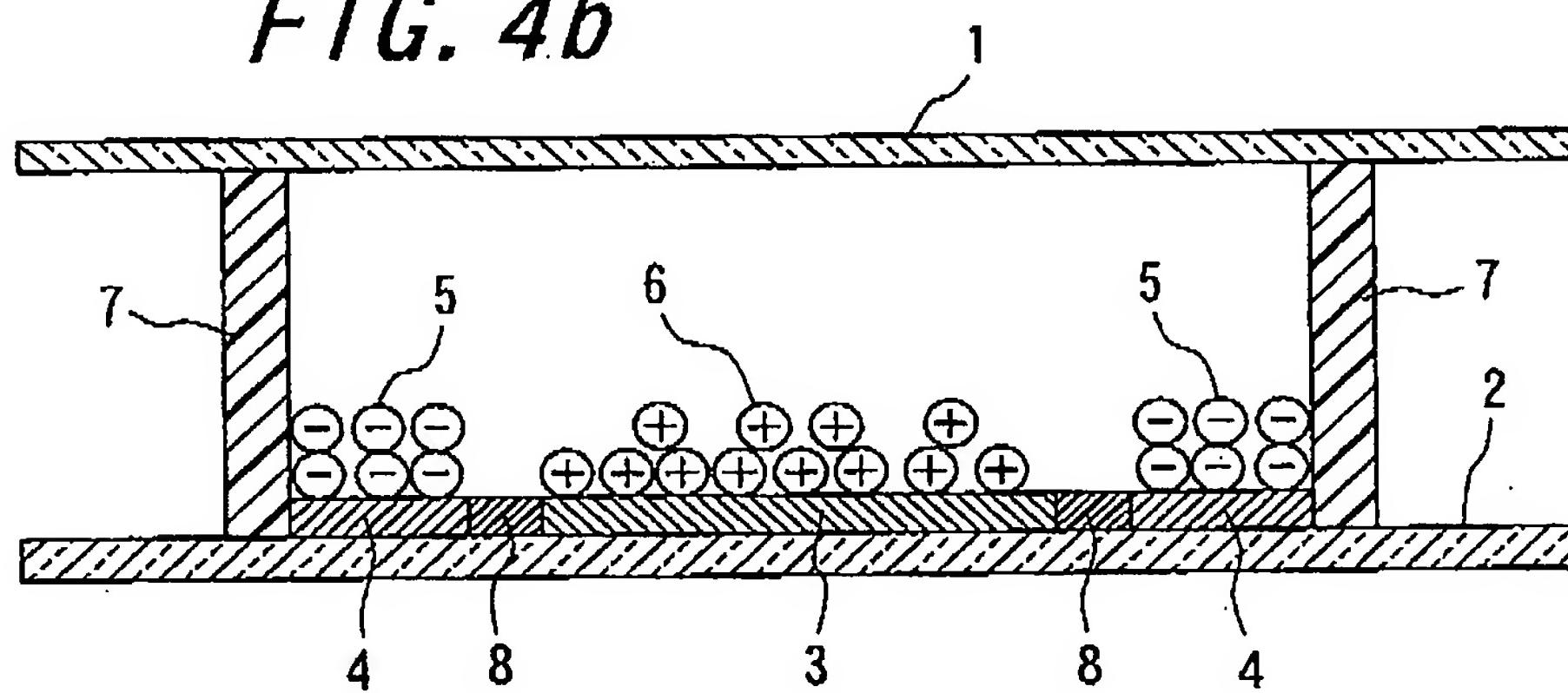
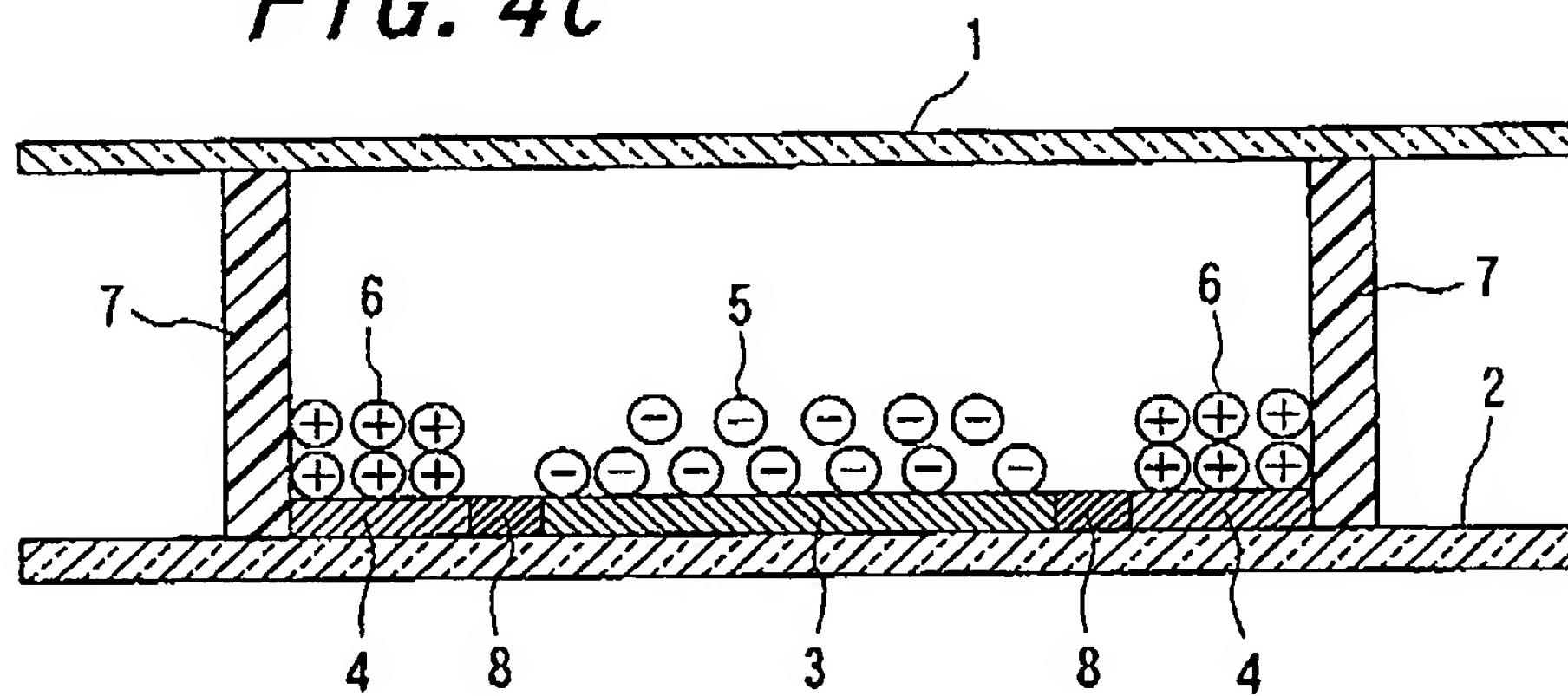
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FIG. 3



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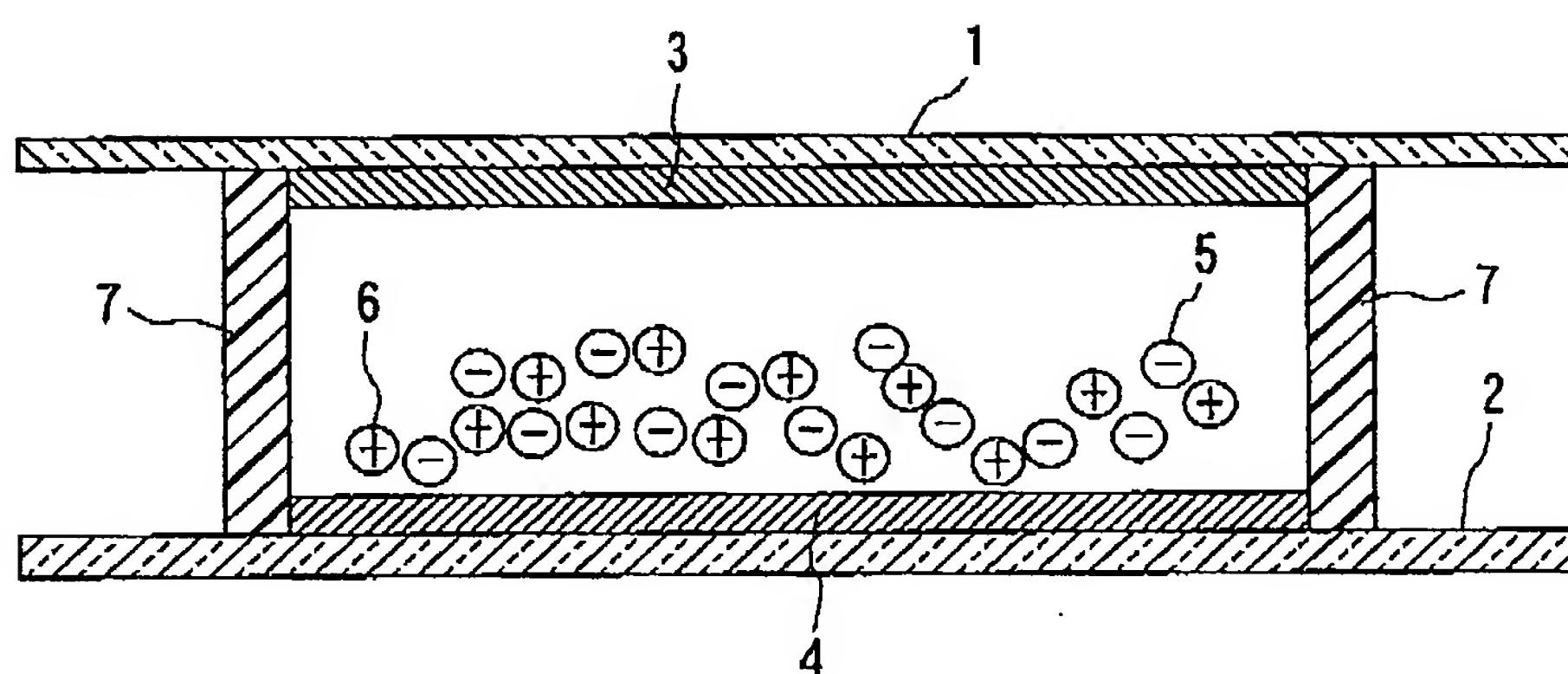
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FIG. 4a*FIG. 4b**FIG. 4c*

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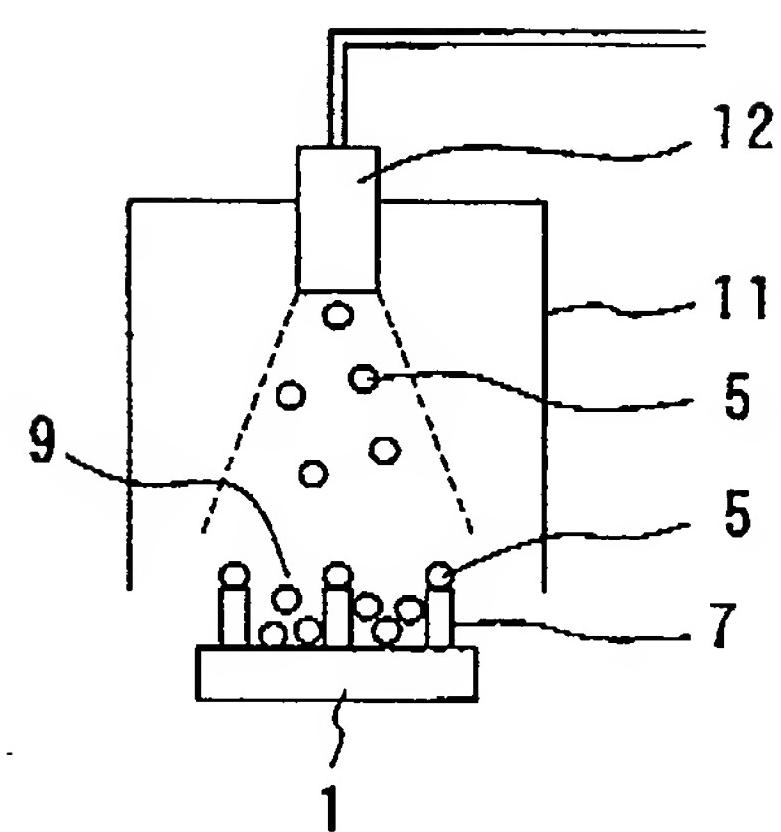
FIG. 5



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FIG. 6



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FIG. 7

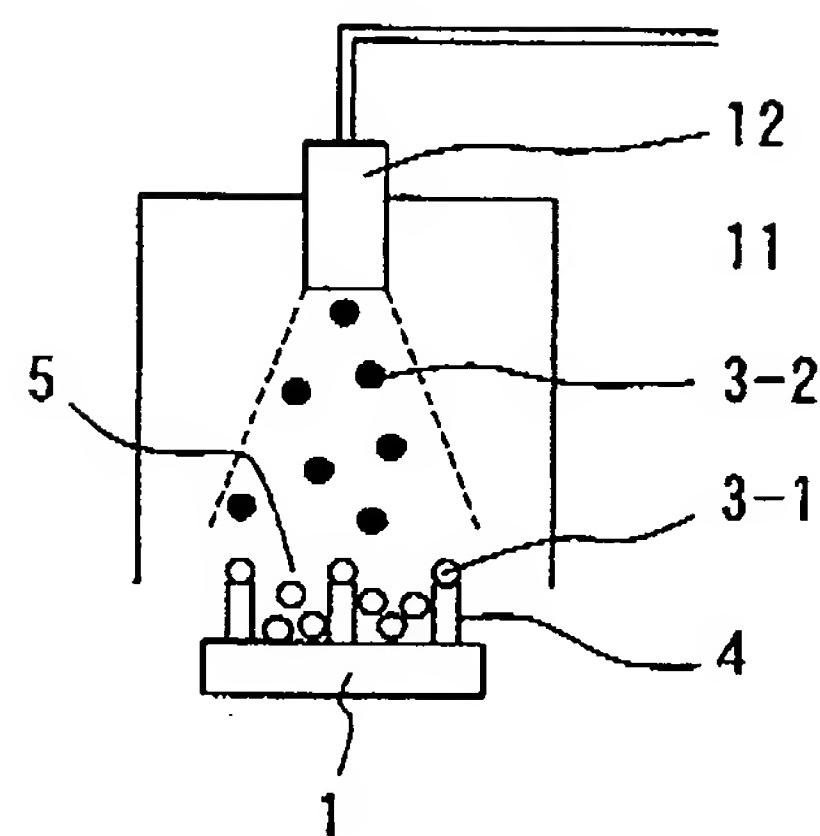
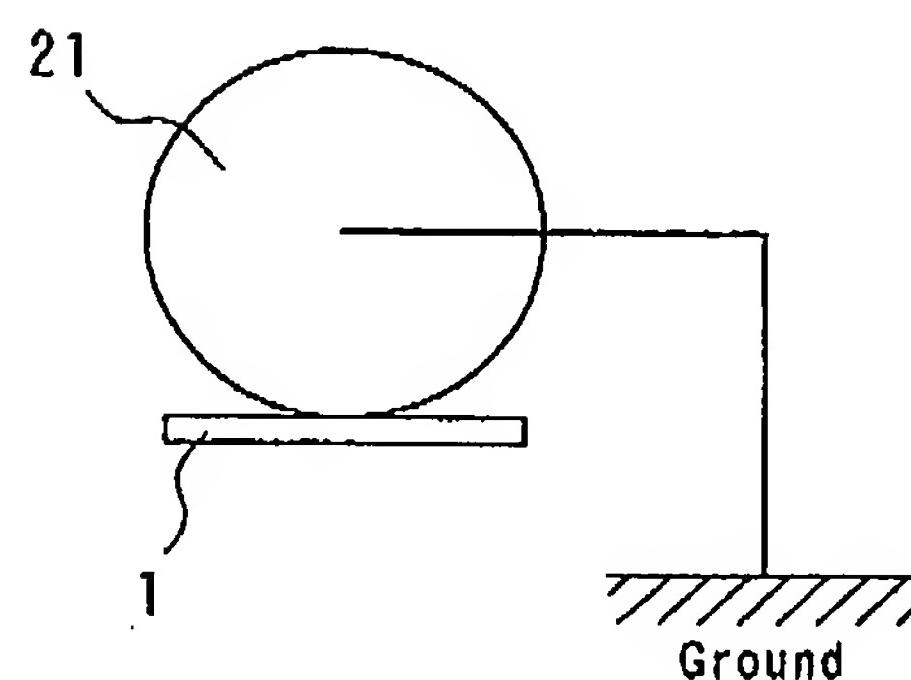


FIG. 8



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FIG. 9a

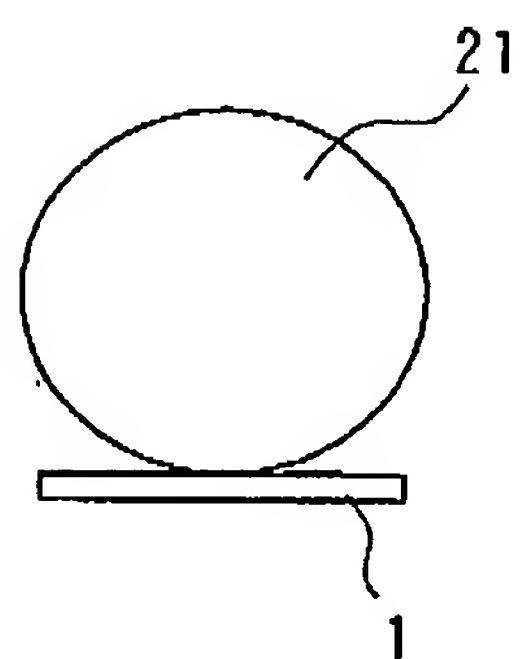


FIG. 9b

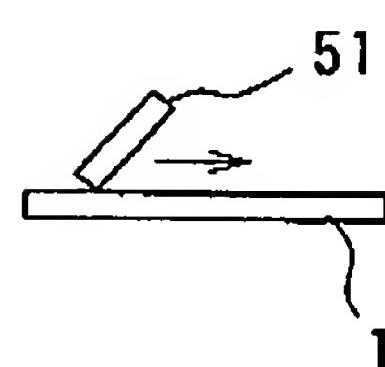


FIG. 9c

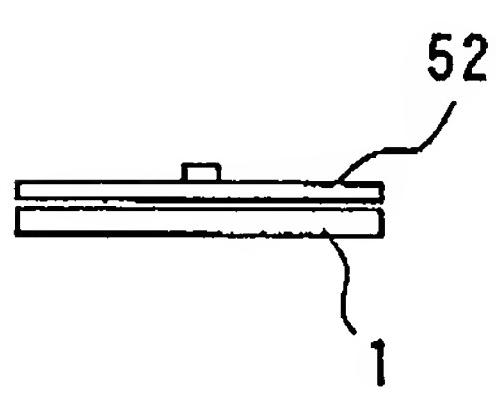
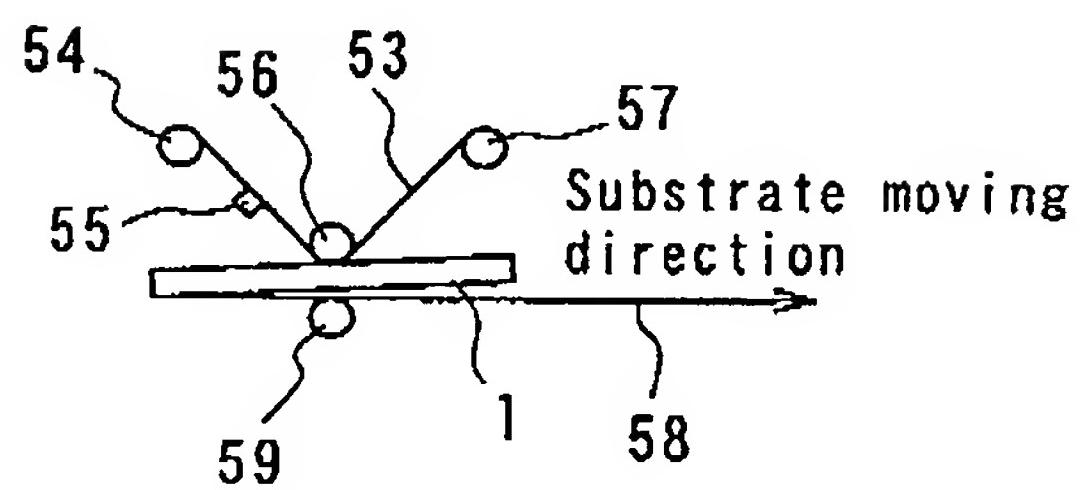
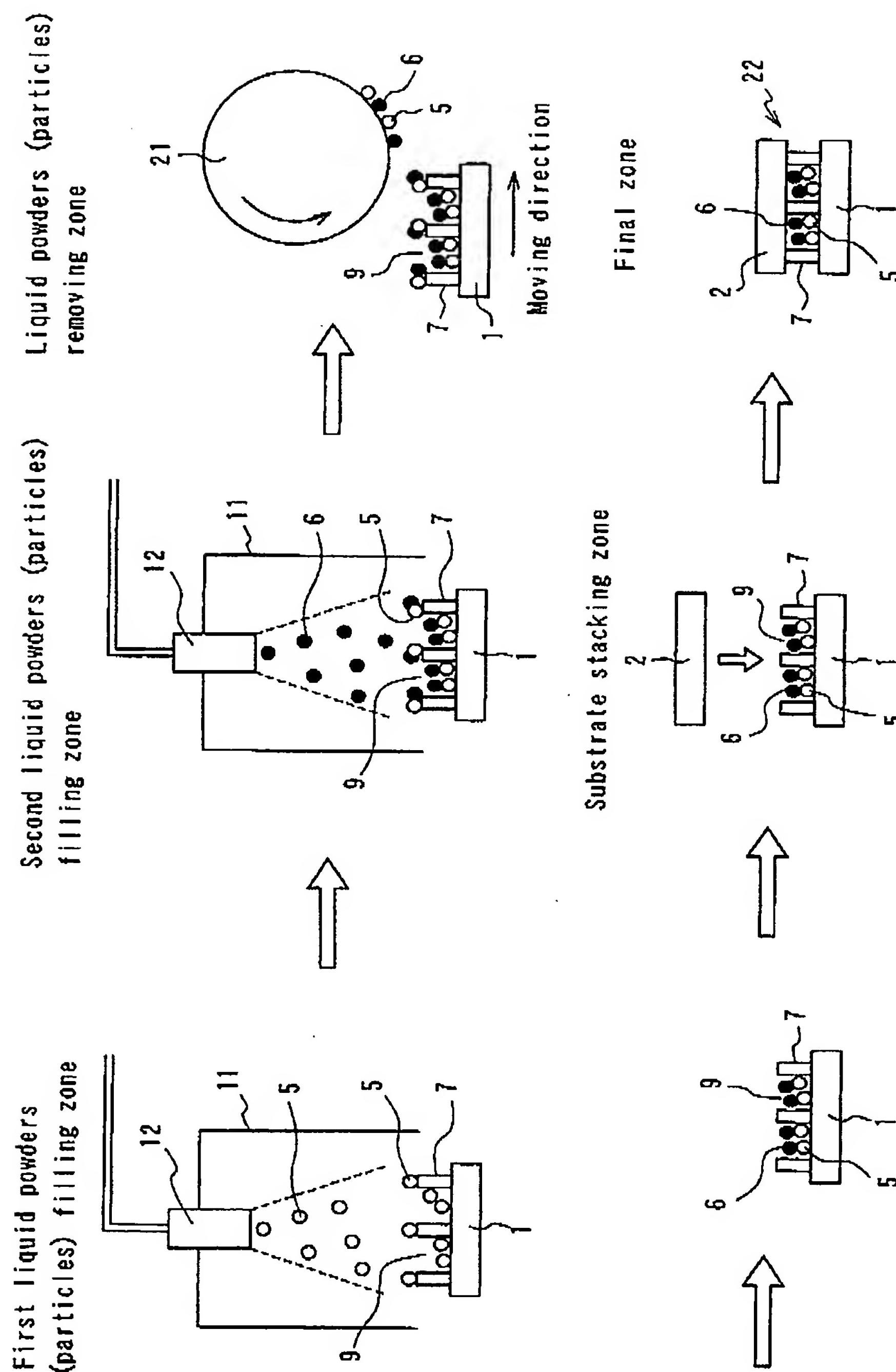


FIG. 9d



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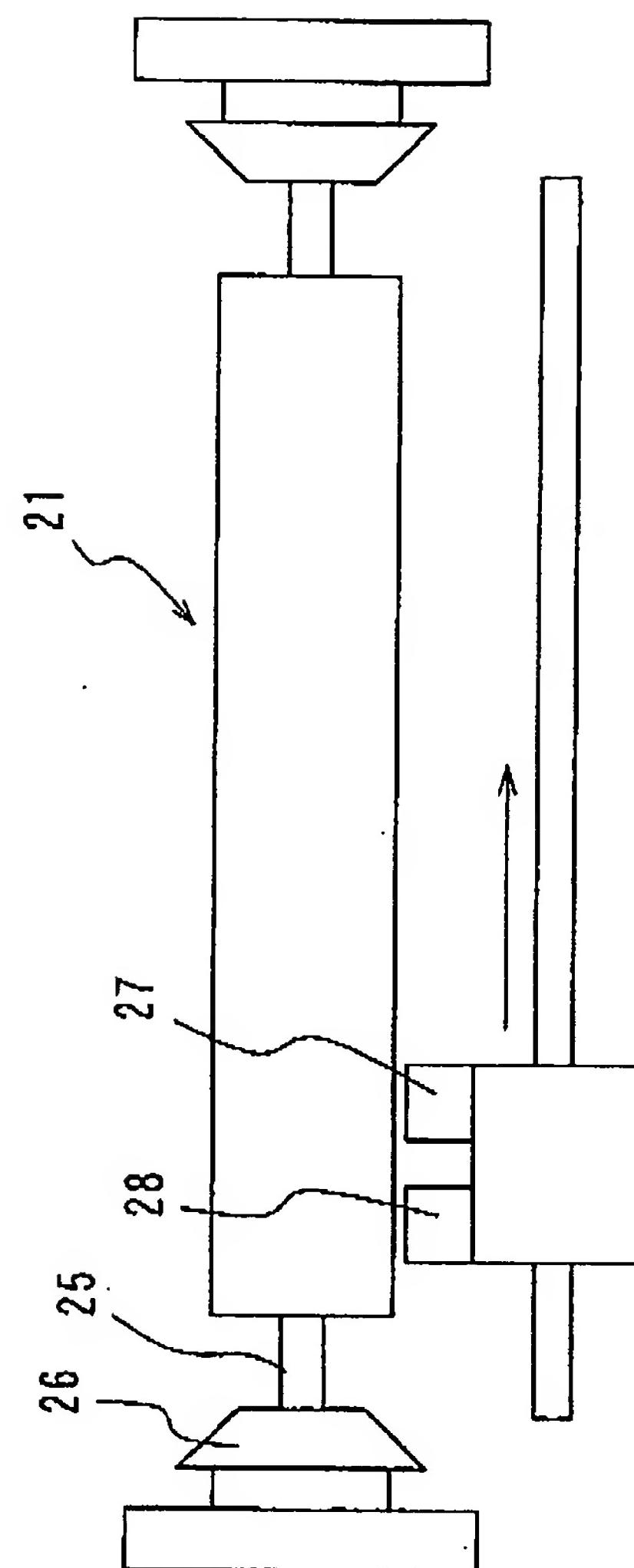
FIG. 10



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FIG. 11

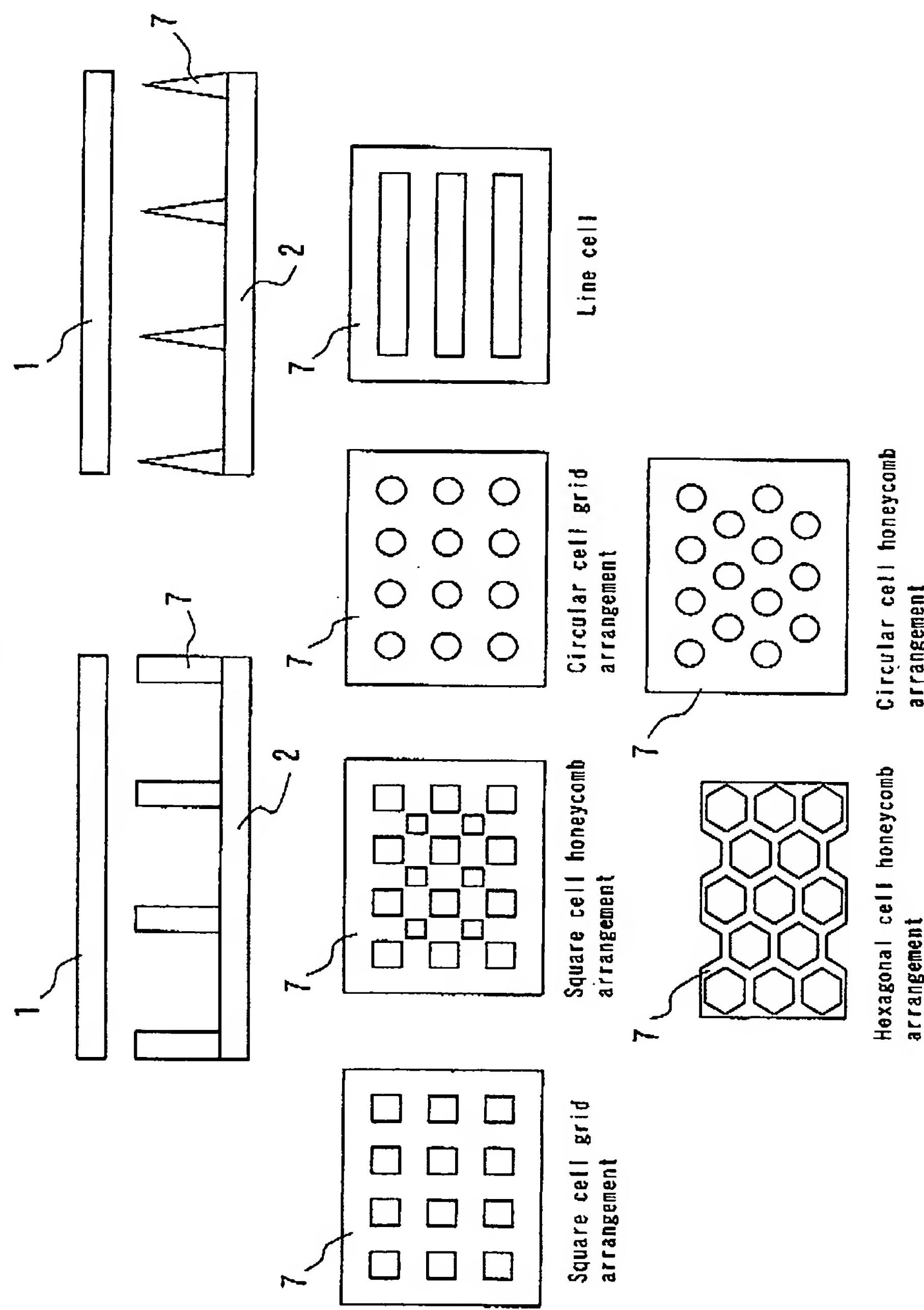


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FIG. 12

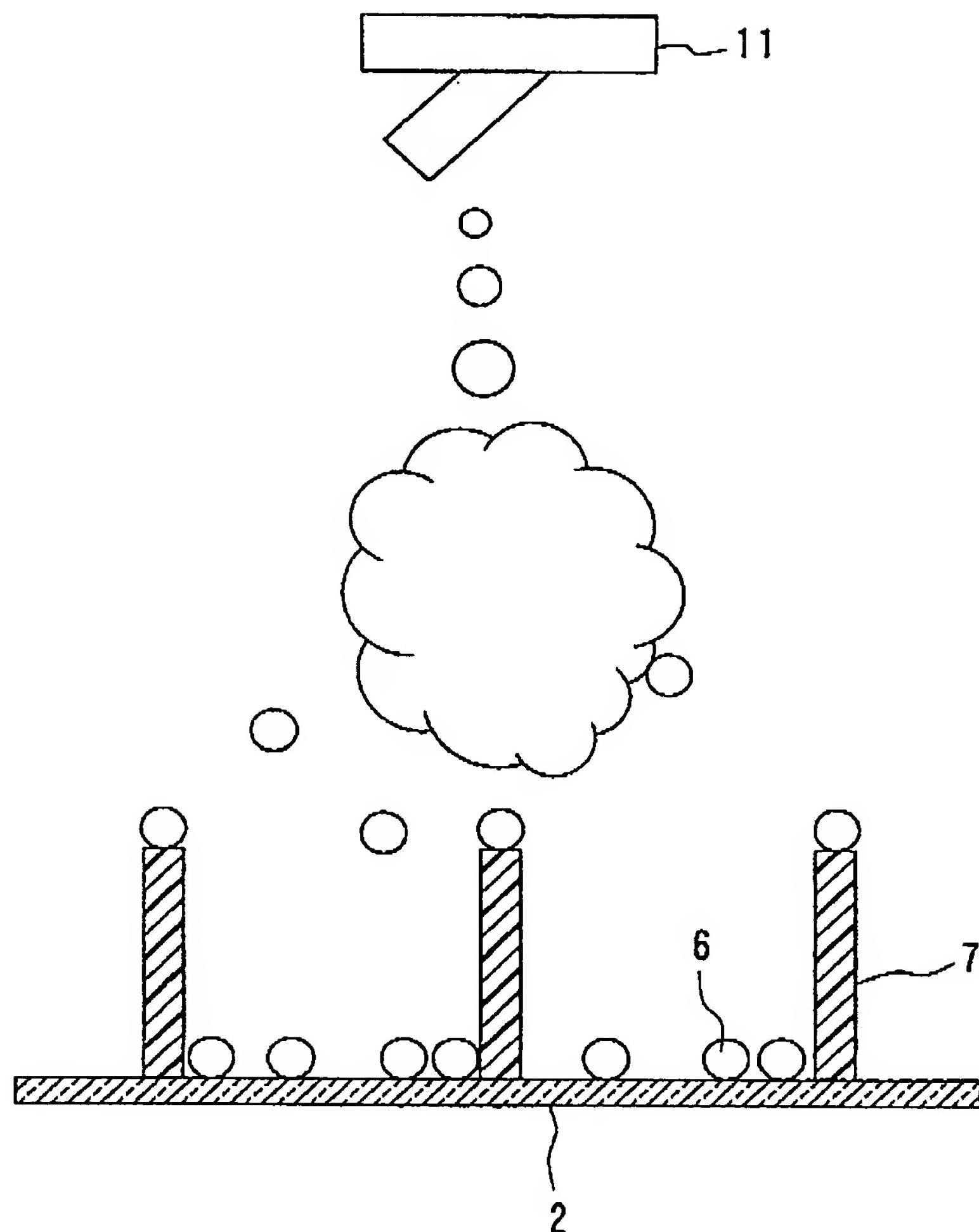


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FIG. 13

(1) Filling step



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FIG. 14a

(2) Removing step

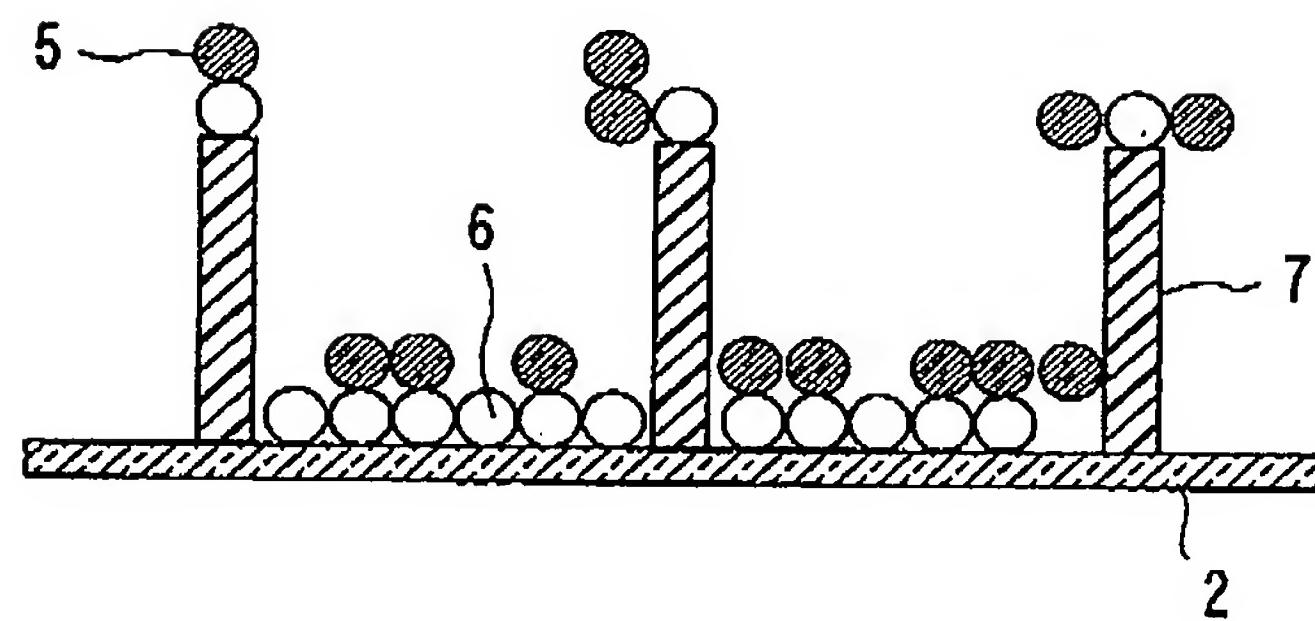
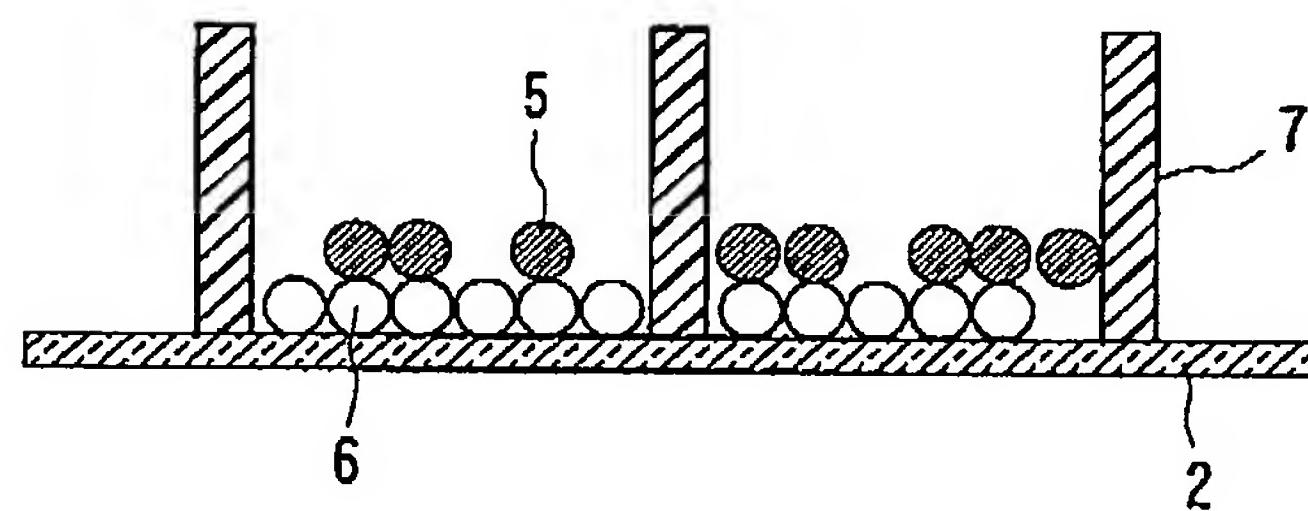


FIG. 14b



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FIG. 15a

(3) Substrate stacking step

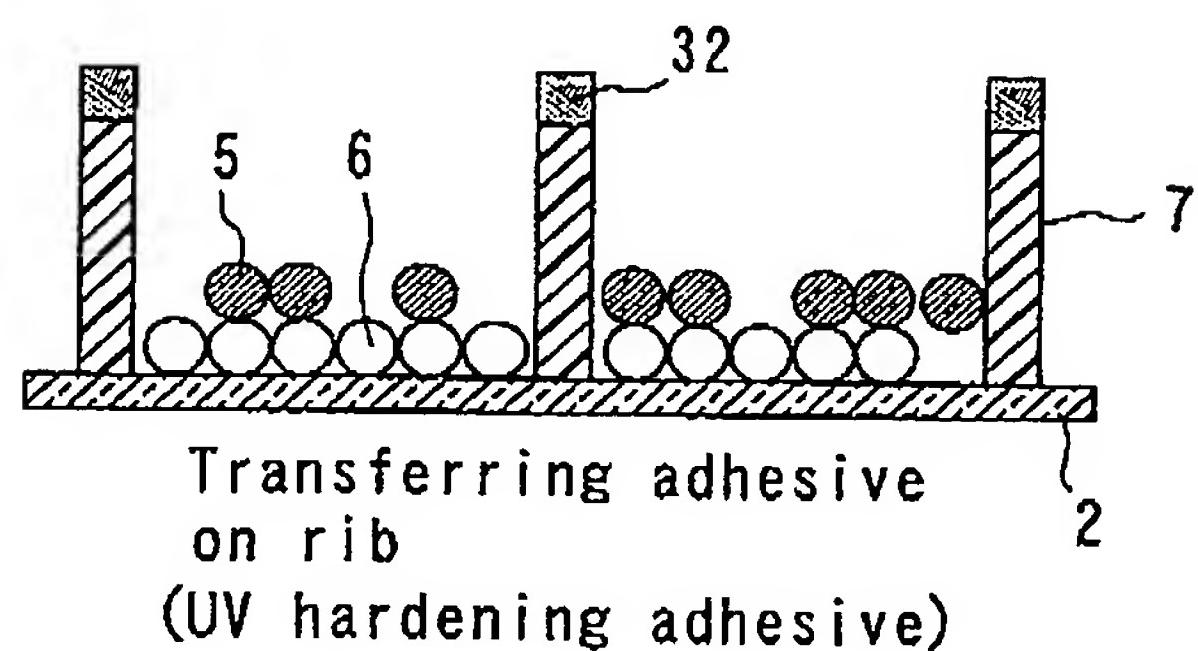


FIG. 15b

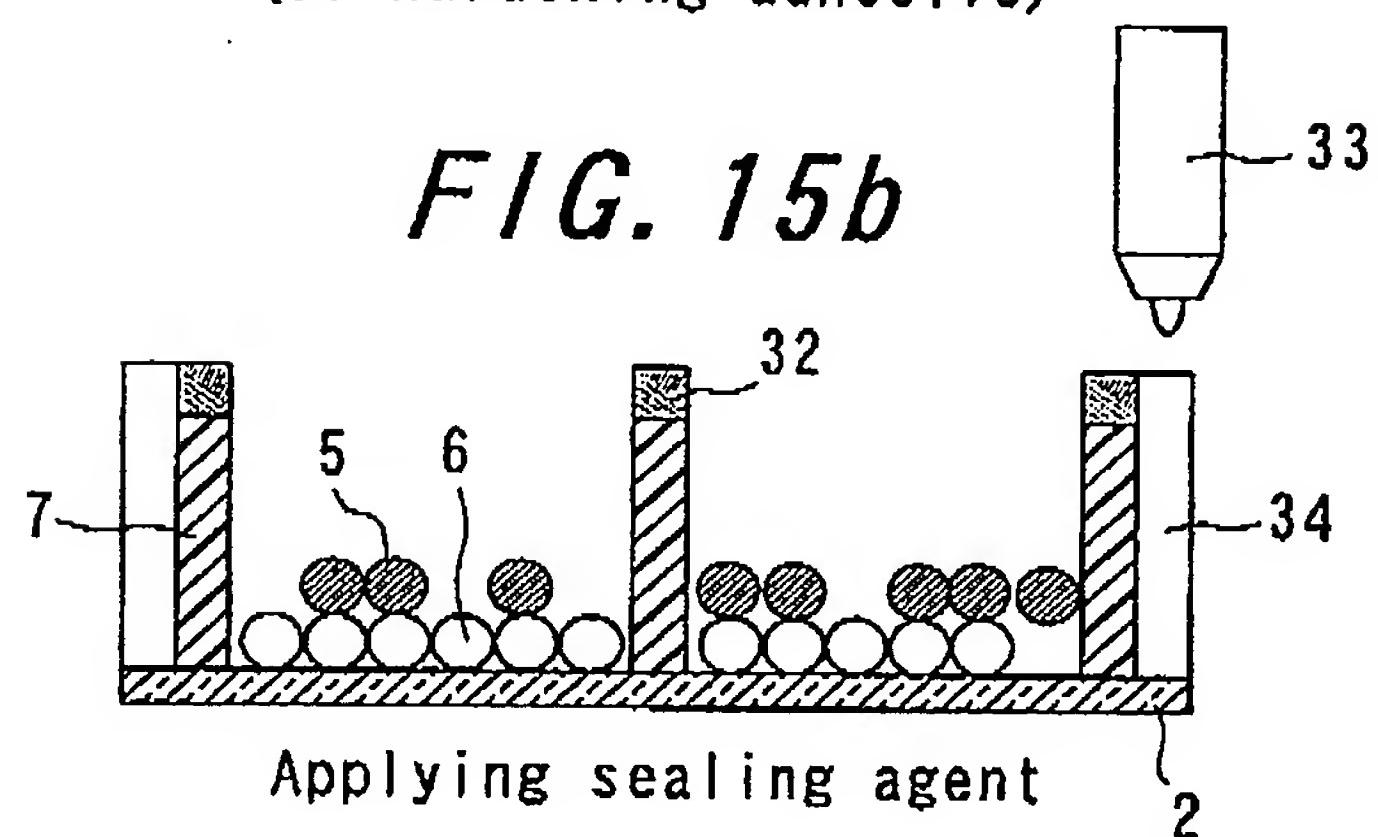
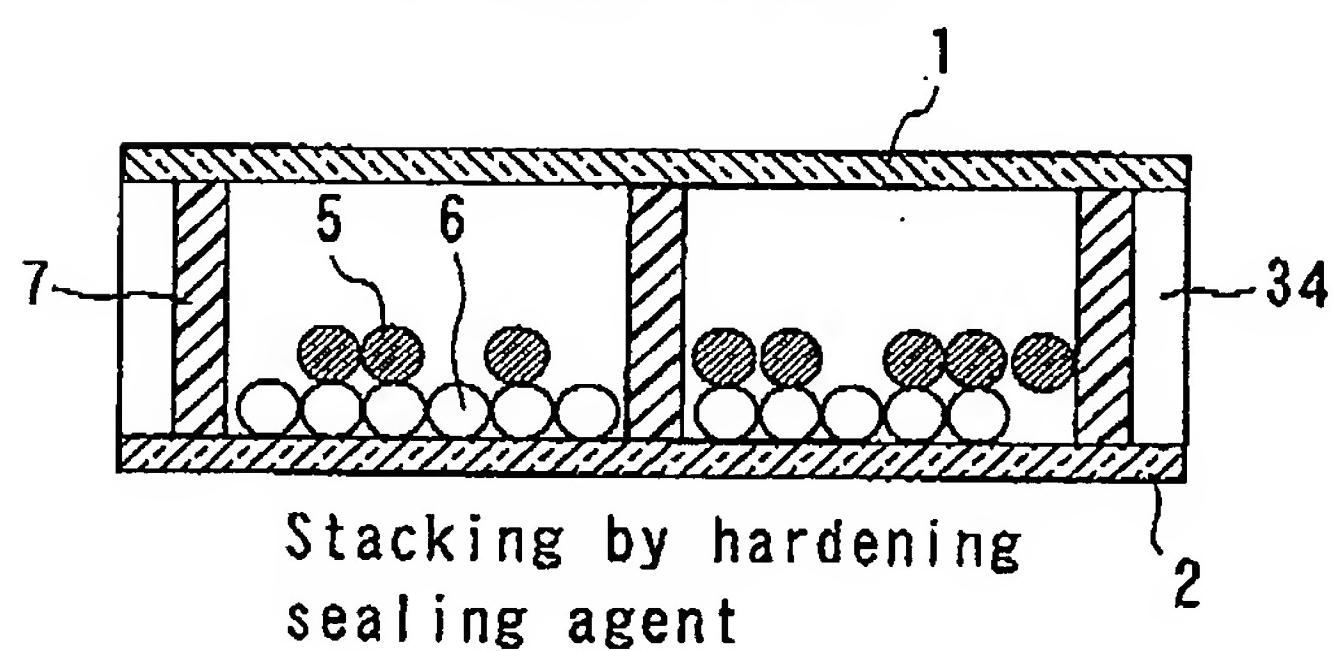


FIG. 15c



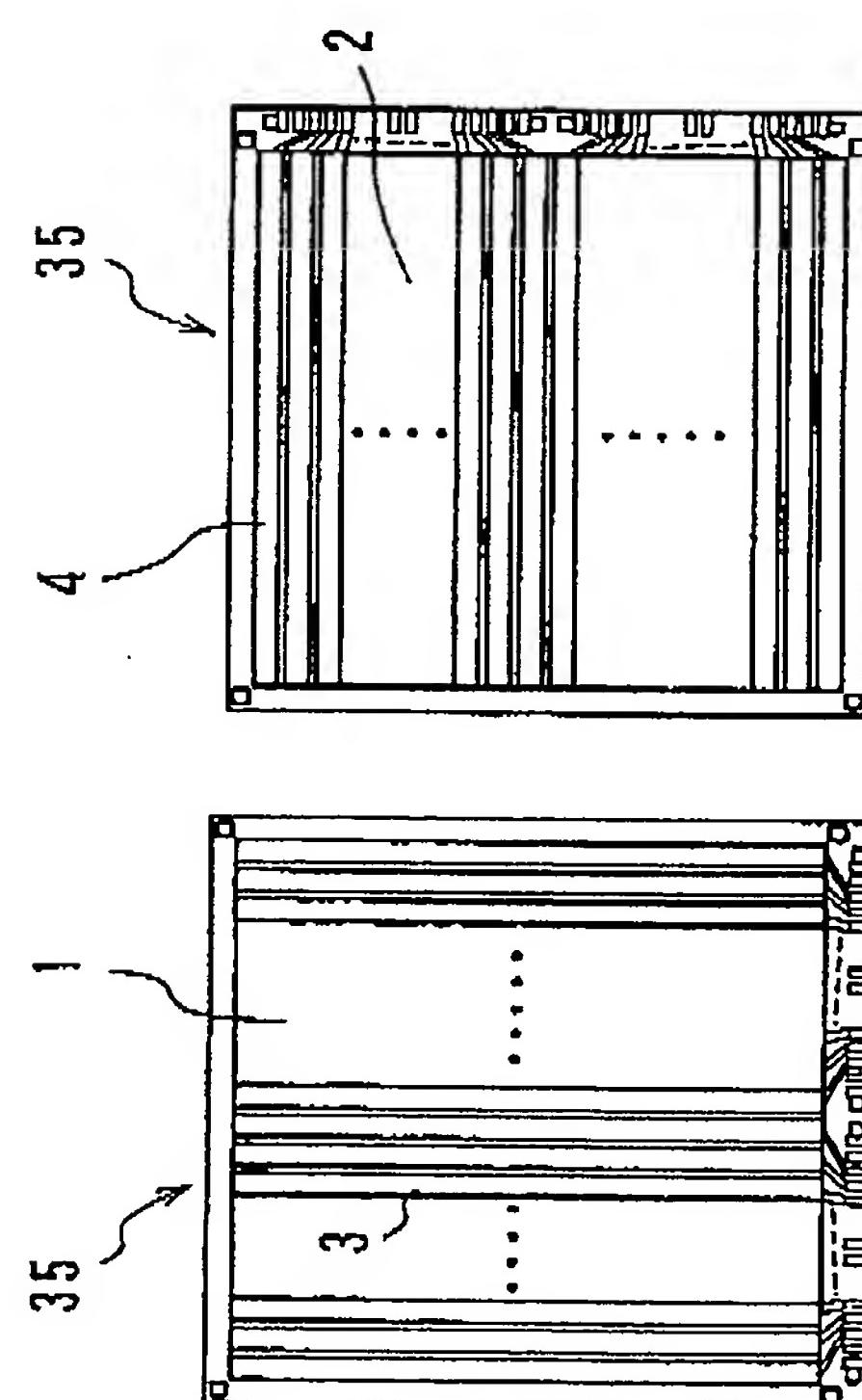
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FIG. 16a FIG. 16b FIG. 16c

(4) or (5) electrode adhering step

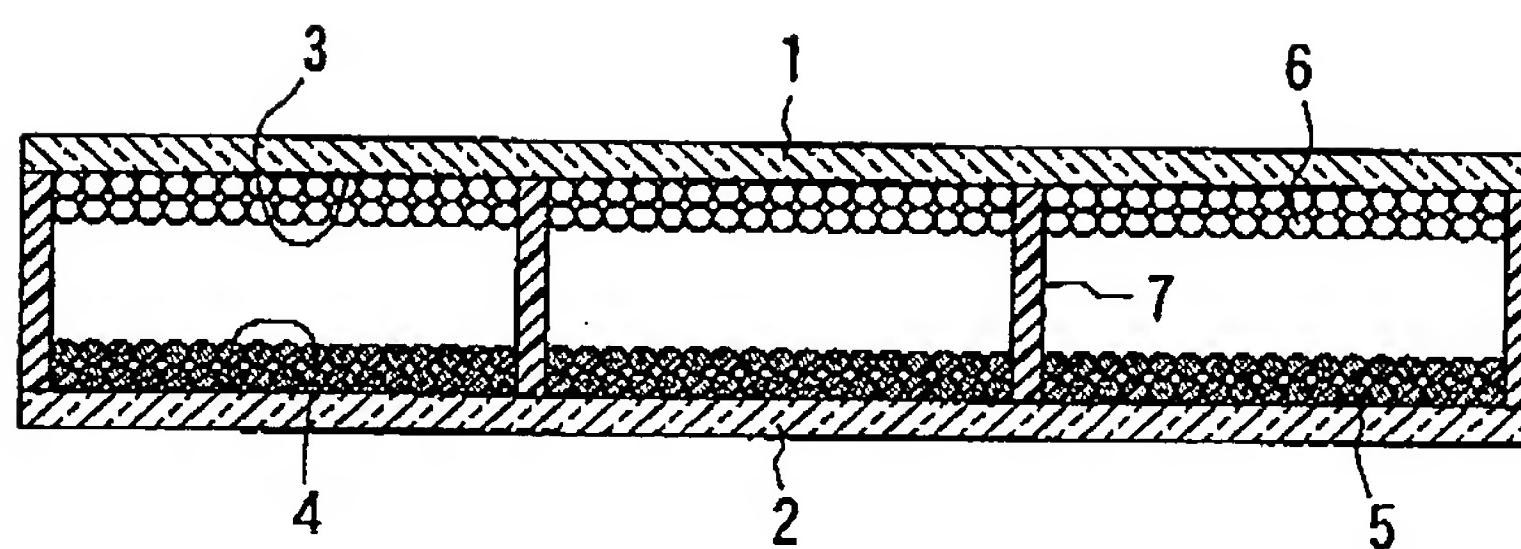


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FIG. 17

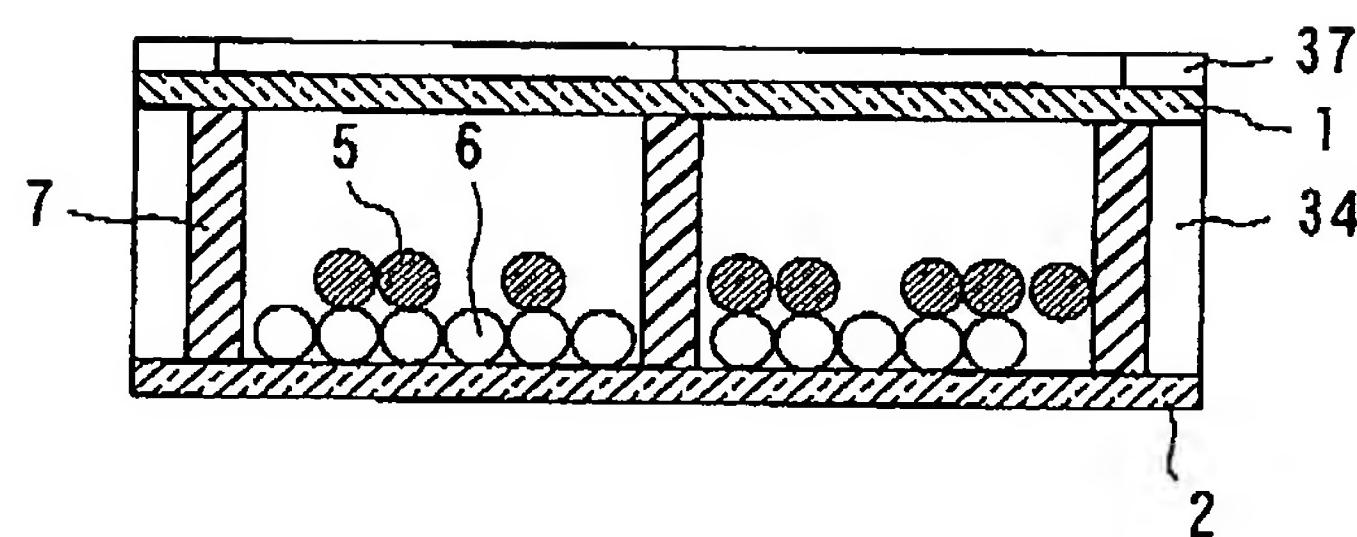
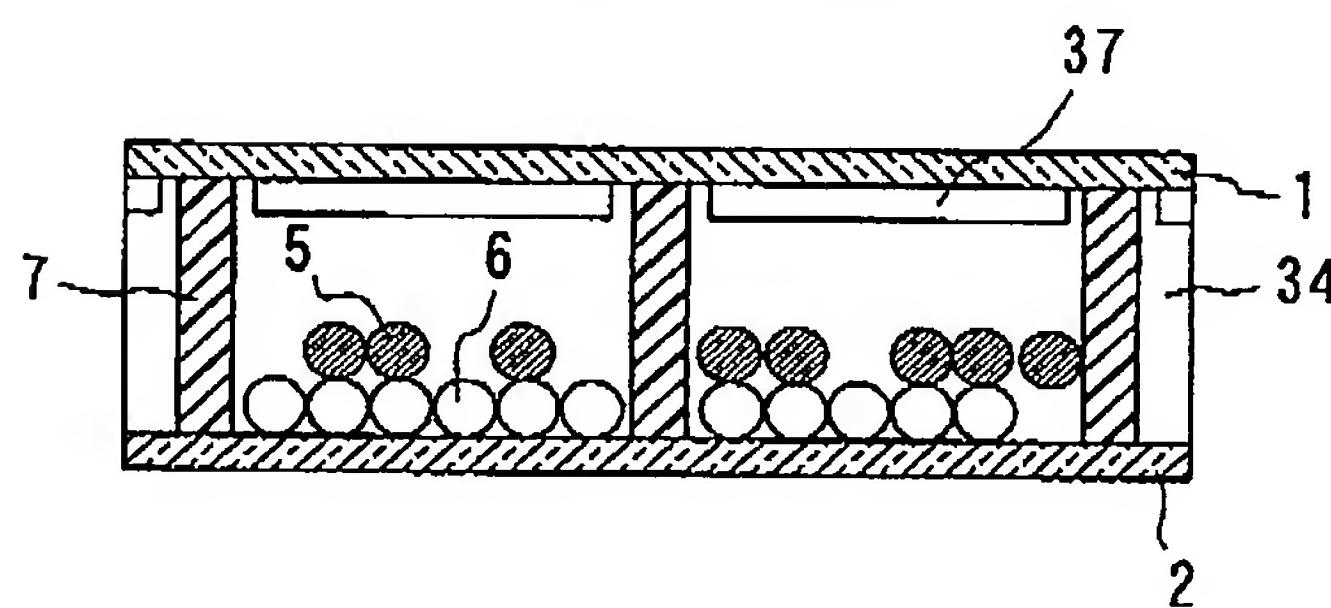


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FIG. 18a

(4) Color filter stacking step

*FIG. 18b*

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FIG. 19

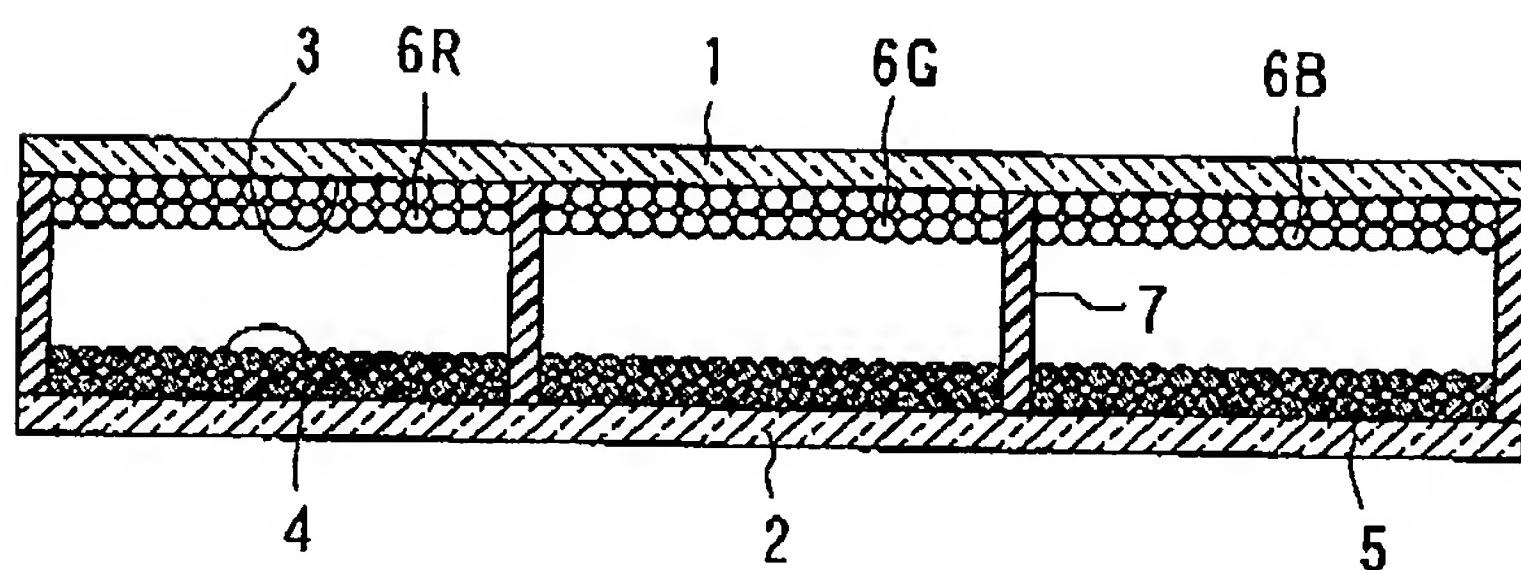


FIG. 20a

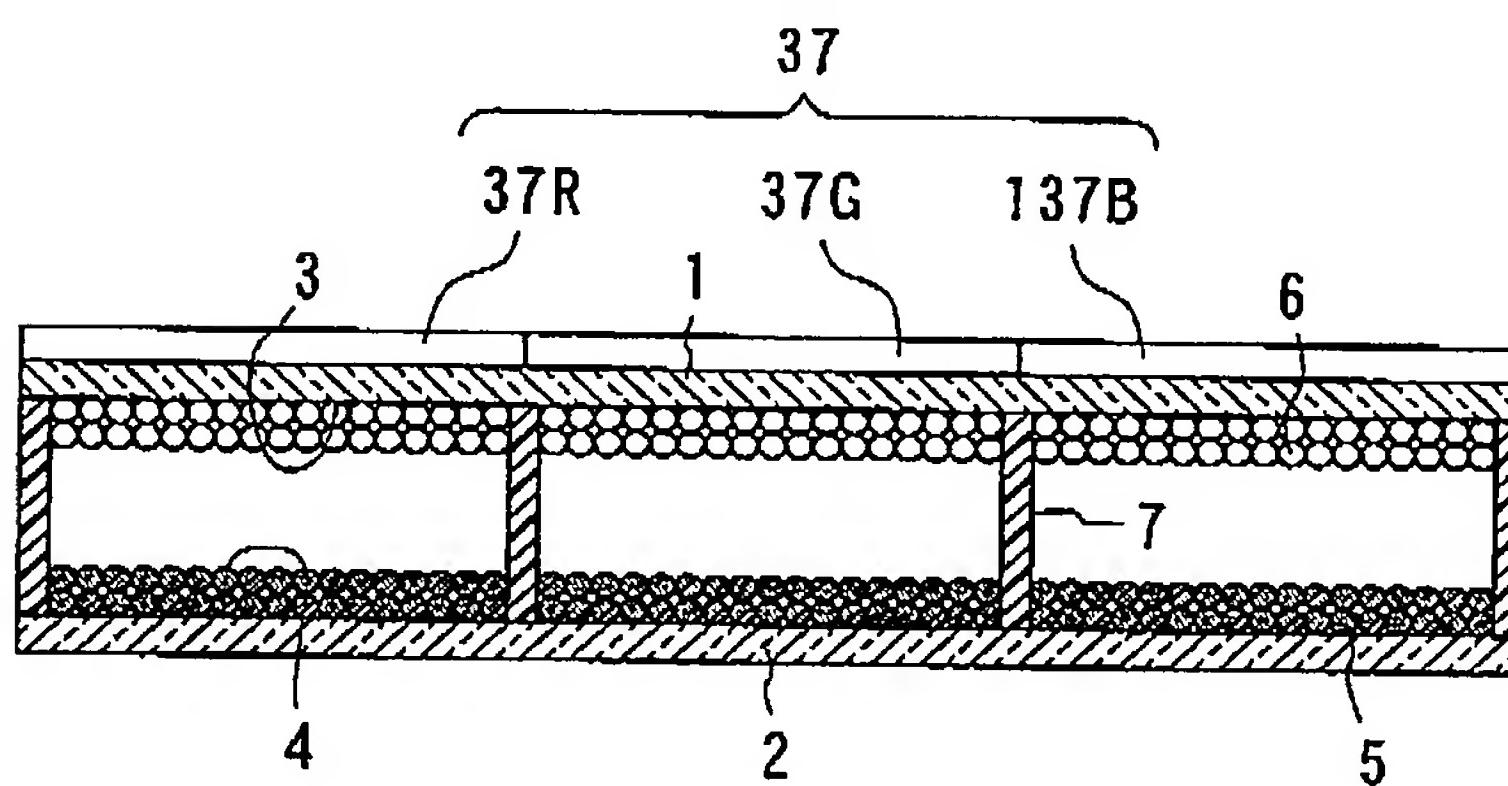
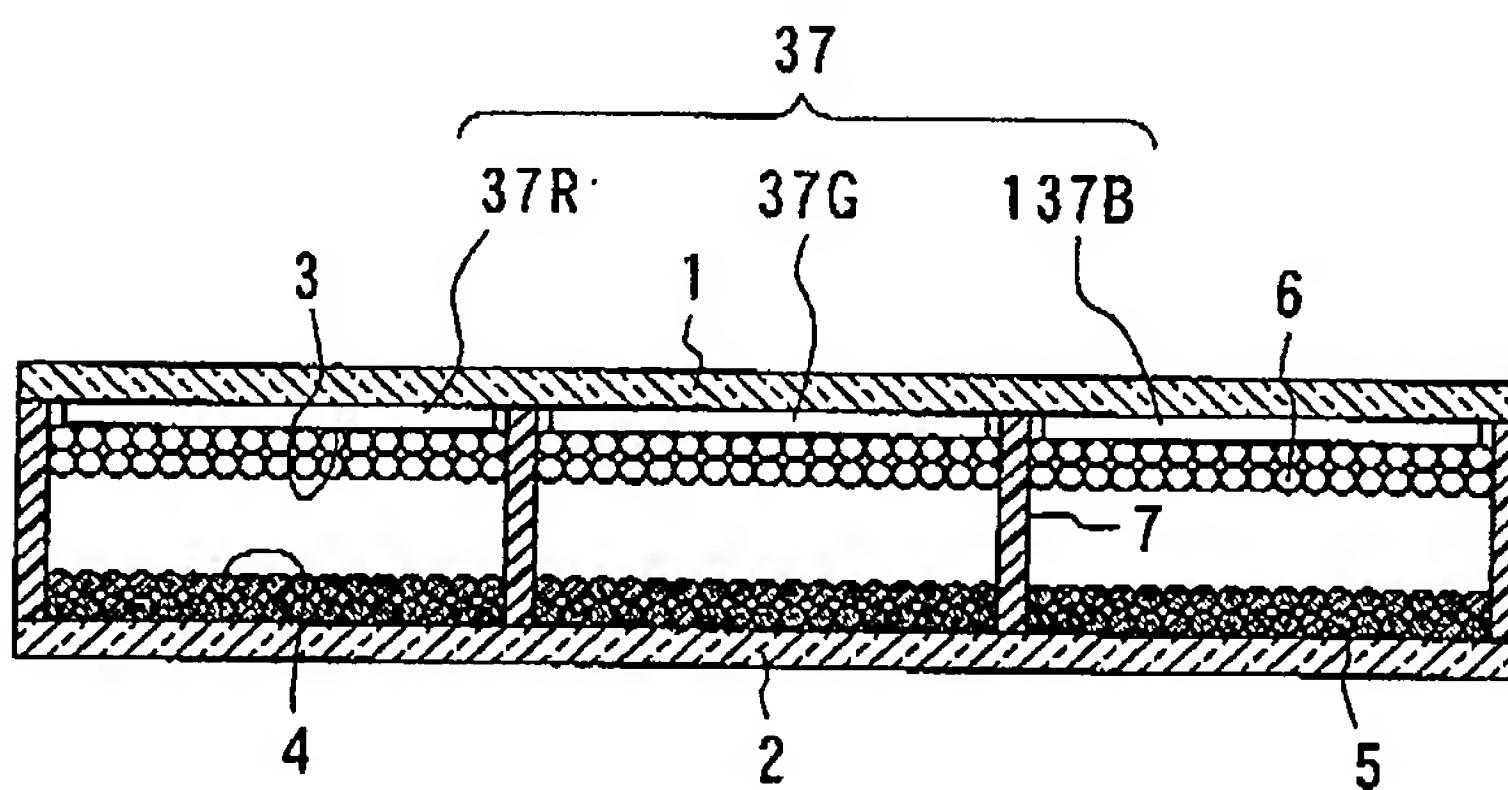


FIG. 20b

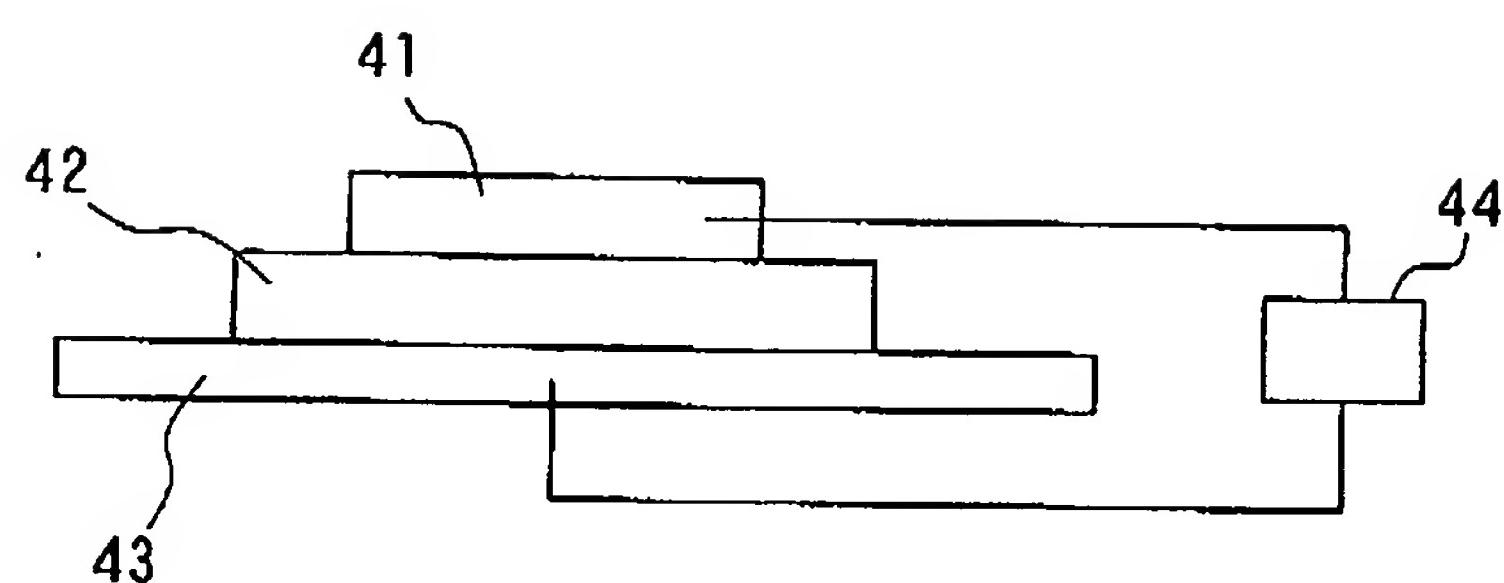


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FIG. 21



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